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Exploring the potential of remote plasma sputtering for the production of L1₀ ordered FePt thin films

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Abstract

Lowering the temperature at which the desirable L1₀ phase forms in FePt thin films is a key requirement in the development of next generation high-density data storage media and spintronic devices. Remote plasma sputtering offers a higher degree of control over the sputtering parameters, allowing the properties of films to be tailored, and potentially can affect the ordering kinetics of the L1₀ phase of FePt. Here, we report a comprehensive study of FePt thin films deposited under a range of temperatures and sputtering conditions. X-ray diffraction and magnetometry investigations show that whilst FePt thin films ordered in the L1₀ phase with high perpendicular anisotropy can be produced using this technique, there is no significant reduction in the required ordering temperature compared with films produced using conventional DC sputtering. Optimally ordered L1₀ FePt films were fabricated when the film was deposited at a substrate temperature of 200°C, followed by post annealing at 750°C.
Introduction

The increase in the areal density of hard disk drives has slowed considerably over the last few years as conventional perpendicular recording matures, with values of approximately 1 Tbit/in² currently available in commercial products [1]. A number of approaches have been deployed to enhance the areal density including shingled magnetic recording [2] and the use of helium gas to decrease the fly height of the read/write head and reduce energy consumption [3]. However, in order to increase areal density significantly a new paradigm is needed. Heat assisted magnetic recording (HAMR) has emerged as the key technology in the quest for higher areal densities [4], as it allows a balance between the thermal stability of the recorded data (dictated by the product of the magnetic anisotropy and grain volume, KuV), the writability of the media (given by the field available from a recording head), and the signal to noise ratio (SNR) of the recovered data (number of grains in a bit); also referred to as the trilemma of magnetic recording [5]. In order to address the trilemma, a recording medium of very high anisotropy is required, which allows the grain size to be reduced whilst still maintaining high thermal stability. However, it is impossible to write data to very high anisotropy media using the limited write field produced by the conventional magnetic write head. HAMR overcomes the issue of writability by heating the recording medium to locally reduce the anisotropy, enabling data to be recorded using the magnetic fields produced by the conventional write head. However, a good SNR on readout is also required, which requires the medium to have a small (<10nm) and well-controlled grain size distribution to enable narrow magnetic transitions to be recorded. \( \text{L}_1 \text{O} \) ordered FePt thin films offer the most promising route to achieving magnetic storage media that satisfies these requirements due to the high perpendicular magnetic anisotropy of \( 7 \times 10^7 \text{ erg/cm}^3 \) in the bulk, the ability to form grains and nanoparticles of a few nanometers diameter, and a Curie temperature of \( T_c \approx 750 \text{K} \) in excess of room temperature [6]. FePt is a binary alloy that can be fabricated with different structural phases and hence exhibit different magnetic properties. In the \( \text{L}_1 \text{O} \) phase the crystal structure of FePt consists of alternate monoatomic layers of Fe and Pt along the (001) direction, providing the high magnetic perpendicular anisotropy that makes these materials ideal candidates for HAMR [6, 7]. However, creating \( \text{L}_1 \text{O} \) ordered FePt thin films with desirable properties remains a significant challenge, since high deposition temperatures (typically in excess of 500°C) are required to form the required level of ordering [8, 9, 10], making such media difficult to fabricate using the industrial processes developed for producing current commercial magnetic hard disks [11]. In addition, precise control of the (001) texture and microstructure of FePt thin films is required [6].

Remote plasma sputtering utilizes a novel plasma sputtering technology - High Target Utilisation Sputtering (HiTUS) - developed by Thwaites [12, 13]. The technique utilises a high intensity plasma that, unlike conventional DC magnetron sputtering systems, is generated remotely away from the target.
Figure 1 shows the main features of remote plasma sputtering, where the remotely generated plasma is brought into the main deposition chamber and is guided to the target by a pair of electromagnets. As the plasma generation is decoupled from the sputtering process and the RF power, the energy of the ions impacting the target can be controlled independently [13]. Due to the ability to tune the target DC bias voltage (0 - 1000V) independently of the plasma generation, the acceleration energy of the Ar$^+$ ions can be more precisely controlled. In addition, by generating the plasma remotely from the target then more uniform target erosion is achieved, which provides economic advantages when using expensive raw materials such as Pt. Control of the grain size and coercivity of CoFe films [13, 14] and of the perpendicular magnetic anisotropy in Co/Pd multilayer thin films [15] have been reported using this deposition technique.

Figure 1. Schematic of the HiTUS remote plasma sputtering approach. The plasma is generated remotely away from the target and is decoupled from the sputtering process. The plasma is brought to incidence on the target by a pair of steering magnets.

Significant literature exists where various methods to control and potentially lower the ordering temperature required to induce the L1$_0$ ordered phase in FePt have been proposed. Examples include changing the Fe concentration [16, 17], adding doping elements such as Cu [18, 19], Au, Ag [20] and Zr [21], strain-induced transformation [22], and the monatomic layer deposition of Fe and Pt [23]. However, the application of remote plasma sputtering for the fabrication of these films remains largely unexplored. The only significant work on remote plasma sputtering of FePt was reported by Tran & Wright [24] who observed the formation of Fe and Pt silicides when annealing FePt thin films deposited on Si substrates at high temperatures. Here we explore the potential of remote plasma sputtering to create L1$_0$ ordered FePt
thin films with modified properties. In particular, we focus on investigating whether depositing FePt at elevated substrate temperatures can reduce the post annealing temperature required to develop the L1\textsubscript{0} phase in FePt thin films deposited using this technique.

**Experiment**

FePt thin films were deposited by remote plasma sputtering using a PlasmaQuest/Lesker system on to single-crystal MgO(001) substrates from a Fe\textsubscript{50}Pt\textsubscript{50} at\% (equatomic) target. The films were sputtered at a RF power of 1kW and at a range of target DC bias voltages (200V, 600V, and 1000V) and substrate deposition temperatures (room temperature, 200°C, 300°C), where the maximum substrate temperature is limited by the capabilities of the substrate heater used. In all cases the MgO substrates were baked at 300°C in-situ for an hour prior to the deposition of the thin film. The vacuum chamber base pressure was better than 8 x 10\textsuperscript{-9} mbar for the deposition at room temperature.

Post annealing of the deposited films was performed using two approaches. In the first approach, the as-deposited films were placed in an Anton-Parr high-temperature oven (under a vacuum of ~10\textsuperscript{-4} mbar) mounted in a Phillips X’pert Pro diffractometer, which allows X-ray diffraction (XRD) measurements to be performed in-situ as the annealing temperature is varied. The fcc to L1\textsubscript{0} transition is not expected to depend strongly on annealing time, in the tens of minutes range, as shown by time-temperature-transformation (TTT) diagrams [25], therefore, an annealing time of 30 minutes was chosen, with a further 45 minutes for the in-situ XRD measurement at each temperature. The XRD measurements used Cu - K\textsubscript{\alpha} radiation of wavelength \(\lambda = 1.5418 \, \text{Å}\), and a PANalytical X’Celerator detector. \(0\text{-}2\theta\) scans provided data on the crystallographic structure of the FePt thin films in the perpendicular (out of plane) direction. In the second approach, fresh samples were annealed ex-situ in a conventional tube furnace for 1 hour in an Ar/10%H\textsubscript{2} reducing atmosphere in order to minimize oxidation, after which XRD measurements were performed using the Philips X’pert Pro diffractometer. In order to investigate the (001) texture of the fabricated FePt thin films the lattice parameters were obtained by analysing the peaks in the XRD spectra [26]. The order parameter is often used as a measure of the level of ordering in a film, which is determined by fitting to the peaks in the XRD data. However, in our XRD data the background contribution along with the available instrument configuration introduces significant uncertainty into the calculated values. Therefore, we investigate the level of L1\textsubscript{0} ordering through the intensity of the fcc FePt (200) and L1\textsubscript{0} FePt (001) and (002) peaks in the XRD data.
The magnetic properties of the thin films were measured at room temperature using a MicroSense Model 10 vector VSM, which allows a maximum magnetic field of 2 T to be applied in different geometries. This enables measurements of in-plane and perpendicular (out of plane) hysteresis loops to be made from which values of the saturation magnetization (Ms), coercivity (Hc) and anisotropy can be obtained. All VSM measurements were carried out at room temperature. The anisotropy field, $H_k$, was obtained from the field required to saturate the film in the hard axis direction. In the case where the available applied field of 2T was insufficient to saturate the sample, $H_k$ was estimated by extrapolation.

**Results & Discussion**

**3.1 In-situ studies of FePt ordering**

Initial studies focused on determining the evolution of the L1$_0$ ordering as FePt films deposited using remote plasma sputtering were annealed at increasing temperatures. In this study, a 20nm FePt film was deposited on to an MgO (001) substrate at a target DC bias voltage of 200V, followed by a 5nm Pt capping layer. The substrate temperature was held at 200°C throughout the deposition.

Figure 2 (a) shows the in-plane and perpendicular (out-of-plane) hysteresis loops for the as-deposited film (sample PQ441a) measured by VSM. The hysteresis data shows that the easy axis for the as-deposited field lies in-plane with a measured saturation magnetization of $M_s=1180\text{emu/cm}^3$, which matches the expected value given in literature [7]. The anisotropy field, $H_k$, was measured as the field at which the perpendicular loop (hard axis) saturates, and is approximately 14kOe, which demonstrates that, as expected, shape anisotropy dominates.

The annealing temperature at which the onset of L1$_0$ ordering occurs was determined from the peaks in the XRD data, where the annealing of the samples was performed using the Anton-Paar high-temperature oven. Figure 3 shows the change in the XRD data as the annealing temperature is increased in steps from 300°C to 800°C (sample PQ441a). The presence of L1$_0$ ordering is signified by the presence of a FePt (001) peak, and the presence of perpendicular texture by the presence of a FePt (002) peak in the XRD data. The as-deposited film (bottom line) does not exhibit a (001) diffraction peak, which indicates that there is no L1$_0$ ordering present in the as-deposited film. The evolution of (001) and (002) peaks is observed as the annealing temperature is increased, with the (001) peak becoming more clearly defined for annealing temperatures in excess of 450°C. The inset of Figure 3 shows the XRD data for the (200) and (002) peaks for annealing temperatures of 650°C, 700°C and 750°C where a large change in relative intensity occurs. The (002) peak first appears for an annealing temperature of 700°C, where the (200) and
(002) peaks are combined, which along with the presence of the (001) peak indicates that there is perpendicular ordering with some disorder still present [27]. As the annealing temperature is increased an increase in the intensity of the (001) and (002) peaks is observed along with a reduction in the intensity of (200) peak, with the (001) and (002) peaks becoming sharper and more pronounced for annealing temperatures of 750°C and 800°C. As the amplitude of the (001) and (002) peaks is similar at these annealing temperatures we can conclude that a temperature of 750°C is sufficient to order the film.

Figure 2 In-plane (dotted) and perpendicular (solid) hysteresis loops for a FePt thin film measured using VSM (sample PQ441a), (a) as deposited, and (b) after annealing at 800°C.
Figure 3. Waterfall graph of the perpendicular (out of plane) XRD data for the 20nm thick FePt film with a 5nm Pt capping layer (PQ441a) deposited using a target DC bias voltage of 200V and at a substrate temperature of 200 °C. The XRD data at room temperature and a range of annealing temperatures from 300 °C to 800 °C, is shown. The inset shows fitted curves for the (200) and (002) peaks at annealing temperatures of 650°C, 700°C and 750°C where evidence of L1₀ ordering first appears.

Figure 2 (b) shows in-plane and perpendicular hysteresis loops measured at room temperature by VSM after annealing at 800°C. The hysteresis data shows that there is still an in-plane component, which indicates that not all the film is ordered out of the plane. This is evidenced by the appearance of a small fcc FePt (200) component in the XRD data (see RT (after) in Figure 3) and a shifting of the angle of the (002) peak to lower angles, which is an indication of bipolar ordering where there are fcc disordered regions in the homogeneously ordered film [28]. A reduction in $M_s$ from 1180 to 865 emu/cm³ is observed compared with the as-deposited sample. Reductions in $M_s$ are frequently observed in ordered FePt thin films and are most likely due to oxidation from the substrate or breakdown of the capping layer due to the relatively poor vacuum in the XRD temperature stage. Diffusion of Pt from the capping layer, resulting in a change in composition of the film, may also play a role in the reduction of $M_s$. 
Consequently, to investigate these films further we performed subsequent annealing studies using a traditional tube furnace in a controlled Ar/H\textsubscript{2} atmosphere in order to minimize any degradation resulting from oxidation/contamination of the film.

### 3.2 Effect of substrate temperature and target DC bias voltage – anneal at 700°C

Using the information gained from the in-situ XRD study described in section 3.1, we explored how the substrate temperature and target DC bias voltage affect the ordering in FePt thin films when the films are annealed at 700°C. A matrix of 10nm thick FePt thin films with no capping layer were sputtered at different substrate temperatures (room temperature (RT), 200°C, and 300°C), and different target DC bias voltages (200V, 600V and 1000V) as listed in Table 1. Figure 4 (a) shows a representative loop for the film deposited at 200°C/200V (PQ468b); all the as-deposited films exhibit a similar in-plane anisotropy.

<table>
<thead>
<tr>
<th>Target DC bias voltage</th>
<th>Room Temperature</th>
<th>200°C</th>
<th>300°C</th>
</tr>
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<tr>
<td>200V</td>
<td>PQ463b</td>
<td>PQ468b</td>
<td>PQ458b</td>
</tr>
<tr>
<td>600V</td>
<td>PQ464b</td>
<td>PQ466b</td>
<td>PQ459b</td>
</tr>
<tr>
<td>1000V</td>
<td>PQ465b</td>
<td>PQ467b</td>
<td>PQ460b</td>
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</table>

**Table 1.** Matrix of FePt samples deposited using a range of target DC bias voltages and substrate temperatures.

Calculated uniaxial anisotropy, K\textsubscript{u}, values \[14, 29, 30\] for the as-deposited films are plotted in Figure 4 (b) as a function of substrate temperature and target DC bias voltage. The films exhibit a low anisotropy phase, dominated by shape anisotropy, which reduces as the target DC bias voltage is increased, and is largely independent of the substrate temperature. XRD data for the as-deposited films (not presented) show a disordered fcc FePt (200) diffraction peak and no FePt (001) and (002) peaks demonstrating the absence of any ordering in the films. The XRD data were analyzed by fitting a Gaussian function to the (200) peaks and the c-lattice parameter was calculated \[31\] as shown in Figure 4 (c). The shaded region represents the range of published values for the c-lattice parameter for disordered FePt thin films. The c-lattice parameter depends on film thickness and varies from 3.85Å for the bulk to 3.8Å for thin films \[10, 32, 33\]. In all cases the c-lattice parameter is less when higher substrate temperatures are used, suggesting that depositing at a higher temperatures supports ordering in the film. The smaller c-lattice constant for
the sample deposited at a target DC bias voltage of 200V suggests a greater level of ordering in the film. The reduction of the c-lattice parameter correlates with a higher anisotropy in these films, Figure 4 (b), which gives further evidence for some level of ordering in the film. However, shape anisotropy still dominates resulting in a net in-plane anisotropy. At higher target DC bias voltages the measured c-lattice constant is within the range of values associated with a disordered FePt phase, which agrees with the lower anisotropy values calculated for these samples, Figure 4 (b).

**Figure 4.** Data for the as-deposited thin films deposited using a range of target DC bias voltages for a range of substrate temperatures. a) VSM measurement for the as-deposited FePt thin film for a target DC bias voltage of 200V and at a substrate temperature of 200°C (PQ468b). b) calculated uniaxial anisotropy, $K_u$, and c) measured c-lattice constant of the as-deposited films as a function of target DC bias voltage and substrate temperature during deposition. The shaded region in c) represents the range of c-lattice parameters for disordered FePt for bulk and thin film [10, 32, 33].

The XRD results reported in section 3.1 demonstrated that for FePt films deposited at a substrate temperature of 200°C there was evidence of L1₀ ordering when these samples were post-annealed at temperatures in excess of 700°C. We take this temperature as a starting point in order to investigate the properties of this matrix of films, which have been annealed in the tube furnace for 1 hour. The matrix of films is then fully characterized by XRD and magnetometry.
Figure 5 shows the perpendicular hysteresis loops for the samples after annealing at 700°C. The hysteresis loops exhibit strong perpendicular hysteresis indicating the presence of the perpendicular L1₀ ordered phase after annealing. However, there is a small magnetization step ("kink") around remanence that is an indicator of the presence of a softer magnetic phase. There are a number of potential causes of these unwanted phases including contamination, isolated, disordered, low anisotropy regions leading to superparamagnetic behavior [34], or low anisotropy regions where the demagnetization field causes reversal nucleation [35].

Figure 5. Perpendicular VSM measurements of the FePt thin films after annealing at 700°C for target DC bias voltages of (a) 200V, (b) 600V, and (c) 1000V, as a function of deposition temperature.

Figure 6 shows the effect of the substrate temperature and target DC bias voltage on the coercivity, Hₘ, and anisotropy, Kₐ, for all the films in the matrix at after annealing. Figure 6 (a) shows that the samples
annealed at 700°C exhibit an increase in coercivity as the DC target bias voltage is increased. In the case of anisotropy, Figure 6 (b), the annealing of the samples has resulted in the formation of a high perpendicular anisotropy phase in contrast to the shape dominated in-plane anisotropy of the as-deposited film (Figure 4). The highest anisotropy being observed for the film deposited at a target DC bias voltage of 200V and at higher substrate temperatures. The anisotropy field, $H_k$, was estimated by linear extrapolation of the hard axis (in-plane) loop and was found to be greater than 20kOe for all the thin films produced.

![Graphs showing coercivity and uniaxial anisotropy](image)

Figure 6. Coercivity, $H_c$, and calculated uniaxial anisotropy, $K_u$, of the FePt thin films after annealing at 700°C (a) and (b), and 750°C (c) and (d), as a function of target DC bias voltage and substrate temperature.

Figure 7 shows the measured XRD data for the films after annealing at 700°C. The presence of a fcc FePt (200) peak in the data is indicative of more disordered grains in the film, as discussed in section 3.1. In the case of the film deposited at a target DC bias voltage of 200V, Figure 7(a), the (200) peak is present for the samples deposited at room temperature and 300°C. However, for the sample deposited at 200°C the intensity of the (200) peak is significantly reduced indicating that the level of ordering in this sample
is much improved. For the samples deposited at a target DC bias voltage of 1000V the presence of the (200) peak leads to combined broad (200) and (002) peaks indicating an increased level of disorder in the film; this could account for the high coercivity in these films, possibly due to domain wall pinning at boundaries between disordered (fcc) and ordered (L1₀) phases [10].

Figure 7. Perpendicular (out of plane) XRD data of the FePt thin films after annealing at 700°C, deposited using a target DC bias voltage of (a) 200V, (b) 600V, and (c) 1000V, as a function of deposition temperature. The 2θ angles of the MgO(002), fcc FePt(200), L1₀ FePt (001) and (002) peaks are shown.

The c-lattice constant for the films annealed at 700°C was calculated and is plotted in Figure 8 (a). The c-lattice constant for bulk L1₀ ordered FePt is c = 3.713 Å [19, 20], which is highlighted as “ideal L1₀ FePt”
in Figure 8, whereas in the thin film case \( c = 3.74 \text{ Å} \) (4.2 – 8.4nm thick films [10]). It can be seen that in all cases the c-lattice constant is close to that expected for the bulk ordered L1\(_0\) FePt, in particular for the case of target DC bias voltages of 200V and 600V, which is a further indication that we have better ordering in these films. However, the c-lattice constant appears to exhibit little variation with substrate deposition temperature.

**Figure 8.** c-lattice constant of the FePt thin films after annealing at (a) 700°C and (b) 750°C as a function of target DC bias voltage and substrate temperature. The solid line indicates the c-lattice constant for ideal bulk L1\(_0\) ordered FePt [19,20].

### 3.3 Effect of substrate temperature and target DC bias voltage – anneal at 750°C

To explore whether further annealing can improve the level of L1\(_0\) ordering in these films, as evidenced by the in-situ XRD study presented in section 3.1, the samples annealed at 700°C were further annealed in the tube furnace at an increased temperature of 750°C for one hour. Figure 9 shows out-of-plane hysteresis loops for all the films in the matrix following the anneal step at 750°C. The loops demonstrate a strong out-of-plane anisotropy where the squareness (\( \text{Mr}/\text{Ms} \)) of the loops is \( \geq 90\% \), which is consistent with a well textured L1\(_0\) ordered phase. The “kinks” evident in the hysteresis data when the films were annealed at 700°C have been removed after further annealing at 750°C, indicating that an annealing temperature of 700°C is not sufficient to fully order the samples.
Figure 9. Perpendicular VSM measurements of the FePt thin films after annealing at 750°C for target DC bias voltages of (a) 200V, (b) 600V, and (c) 1000V, as a function of deposition temperature.

Values of coercivity, $H_c$, and estimated uniaxial anisotropy, $K_u$, for the annealed films are plotted in Figure 6 (c) and (d) respectively. Whilst the coercivity has been reduced after further annealing, the coercivity of the films deposited at a target DC bias voltage of 1000V is still a factor of 2 greater than that of the other films. Comparing Figure 6 (b) ($K_u$ at 700°C) and Figure 6 (d) ($K_u$ at 750°C) there is little change in the anisotropy for these films, apart from a significant increase in $K_u$ for the sample deposited with a target DC bias voltage of 200V, at a substrate temperature of 200°C. As the systematic differences observed in the coercivity for the 1000V films are not reproduced in the anisotropy data, this strongly suggests that the mechanism responsible for higher reversal field in this series of samples results from the granular structure induced by the higher DC bias voltage and not improved ordering in the film.
Figure 10. Perpendicular (out of plane) XRD data of the FePt thin films after annealing at 750°C, deposited using a target DC bias voltage of (a) 200V, (b) 600V, and (c) 1000V, as a function of deposition temperature. The 2θ angles of the MgO(002), fcc FePt(200), L1₀ FePt (001) and (002) peaks are shown.

Figure 10 shows the XRD spectra for the matrix of films after annealing at 750°C and confirms the presence of the L1₀ ordered phase in all the films through the presence of well developed (001) and (002) diffraction peaks. Comparing the XRD data in Figure 7 (annealed at 700°C) with Figure 10 (annealed at 750°C) there is a consistent trend for a reduction in the fcc FePt (200) peak. This is in agreement with the hysteresis data showing that the “kinks” observed for films annealed at 700°C were absent for films annealed at 750°C. However, the change in XRD data with bias voltage is not identical for all films. In
particular, the films deposited at 200V show the least change in XRD data whereas the films deposited at 600V and 1000V show greater changes with the sample deposited at 200°C showing the least (200) peak intensity following annealing at 750°C.

These data confirm the initial in-situ XRD results demonstrating that the ordering process requires annealing temperatures in excess of 750°C to reach completion. The data also show that whilst it may be possible to “fine-tune” the ordering process by careful choice of deposition temperature and bias voltage, remote plasma sputtering does not offer a significant advantage towards creating thin films of L1₀ ordered FePt at lower deposition/anneal temperatures.

Summary & Conclusions

In summary, we have explored the use of remote plasma sputtering to understand if this technique offers advantages over conventional DC magnetron sputtering for the direct fabrication of ordered L1₀ FePt thin films with high perpendicular anisotropy. Initial studies focused on taking XRD measurements in-situ in order to observe the evolution of the L1₀ phase as the annealing temperature was increased. We observed that the ordered L1₀ phase occurred when the samples were annealed for temperatures in excess of 700°C and were fully ordered after annealing at 750°C. In order to explore these results further we produced a number of FePt films that were deposited at a range of target DC bias voltages (200V, 600V and 1000V) and substrate temperatures (room temperature, 200°C, and 300°C) in order to investigate how the deposition parameters affect the ordering, lattice parameters, and magnetic properties of the deposited films, which we determined using VSM and XRD.

The results show that the ordered L1₀ phase in FePt thin films cannot be induced using moderate (up to 300°C) substrate temperatures during deposition. This was highlighted by the absence of any FePt (001) and (002) peaks and strong disordered (fcc) FePt (200) peaks in the as-deposited films. The L1₀ phase can only be induced when the films are post-annealed at temperatures in excess of 700°C as previously identified. Further, we have shown that the deposition temperature has no significant effect on the annealing temperature required to induce the ordered phase. Annealing films at a temperature of 750°C was shown to improve the level of ordering, resulting in well ordered films with strong perpendicular anisotropy. Optimal ordered films, with the highest $K_u$, are shown to be produced when deposited with a target DC bias voltage of 200V and at a substrate deposition temperature of 200°C, followed by post annealing for 1 hour at 750°C. Depositing films at higher target DC bias voltages results in films with
higher coercivities due which we ascribe to domain wall pinning at the boundaries between ordered and disordered states.

We conclude that whilst ordered L1₀ FePt thin films can be produced using remote plasma sputtering, high annealing temperatures are still required to induce the ordered phase. However, the use of remote plasma sputtering still offers other benefits, in particular uniform target utilization, which offers economic advantages over conventional approaches.

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[www.plasmaquest.co.uk]


