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Impact of Fault Current Limiter on VSC-HVDC DC Protection

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Abstract
Multi-terminal Voltage-Sourced Converter (VSC) High Voltage Direct Current (HVDC) transmission is expected to play a key role in future power transmission grids. Compared with AC transmission, DC transmission is more vulnerable to faults due to the lower DC side impedances and sensitive power electronics used in the converters. DC protection issues must be solved before any multi-terminal VSC-HVDC grid can be established. In terms of DC protection, apart from using a DC circuit breaker to interrupt and isolate a DC fault, a Fault Current Limiter (FCL) may be employed to limit the fault current. FCLs could potentially form an important part of any future DC protection system. This paper investigates such a FCL. The main contribution of this paper is that, the resistive type superconducting FCL (SCFCL) is modelled in detail to perform DC protection with the ABB hybrid circuit breaker, in a high fidelity four-terminal VSC-HVDC system model. The simulation results show that the SCFCL has great potential to be employed for DC protection.

1 Introduction
DC transmission is expected to play an important role in linking offshore windfarms to the onshore power system. In Europe, projects like Borwin1 and Helwin2, etc. are based on DC technology to transmit offshore wind power to onshore grid [1, 2]. Projects in China include the Nan’ao and the Zhou’shan projects [3, 4].

There are the two types of High Voltage Direct Current (HVDC) transmission technologies: Current-Sourced Converter (CSC) and Voltage-Sourced Converter (VSC). VSC-HVDC offers advantages over CSC-HVDC for power transmission, such as it can support weak AC systems, its compactness, flexible control of active and reactive power. Additionally, cheaper XLPE cable could be used in a VSC-HVDC system, as power flow reversal in VSC-HVDC is realised without reversing the DC voltage. Thus VSC-HVDC is seen to be a more practical solution for transmitting offshore electrical energy to land. In addition, a multi-terminal DC grid can be established by using VSC-HVDC. Such a grid can potentially improve the reliability of the DC system; it can better handle the intermittent issues caused by the wind energy than a point-to-point system, and a multi-terminal system can maximise the use of converters [5].

The study of HVDC grid capable protection systems is essential in order to enable the development of a multi-terminal VSC-HVDC grid. Using HVDC circuit breakers to interrupt and isolate a DC fault has been discussed in literature, such as in [6-10] and industrial DC circuit breaker prototypes are being produced [7, 11, 12]. A Fault Current Limiter (FCL) may be used to limit the fault current the DC circuit breaker needs to interrupt. FCLs and DC circuit breakers may be required to work together to provide short-circuit protection.

Initially, FCLs were developed to deal with AC system fault currents. Based on [13], the requirements for FCLs are:

- It should have low impedance during normal operation - this will reduce the losses during normal system operation.
- It should limit the fault current within a short period of time.
- It should take a short time to recover after the fault has been cleared.
- The FCL should be designed in a way that the fault current is still limited even if the FCL fails.
- For an offshore application, the FCL should have compact structure and light weight.
- It should be able to be easily scaled up for high voltage applications.

In [14-16], a good analysis has been performed by using the superconducting FCL (SCFCL) to mitigate fault current but their simulations provide limited detail beyond current magnitude reduction. In this paper, the two-level converter is modelled with detailed switching model, the MMC is modelled with detailed equivalent model, the DC cable models are frequency dependent phase models, the DC circuit breakers are the hybrid switching models and the SCFCLs used in this paper are modelled in great detail. Using such a high fidelity VSC system models, along with a proper DC fault detection and location algorithm, this paper studies the impact the SCFCL exerts on the DC protection system and the DC circuit breakers during a DC side fault.

2 System description and modelling
The example system used in this paper is a four-terminal VSC-HVDC system, shown in Figure 1. This system uses both two-level and modular multilevel converter (MMC) topologies. In this system, converter stations A, B and D connect three stiff AC networks, a fourth terminal connects the aggregated windfarm to the system. In steady state, the offshore windfarm is sending 200 MW power to the HVDC system. Converter station A will send 1000 MW to the HVDC system. Converter
stations B and D will receive 700 MW and 500 MW respectively.

2.1 Hybrid DC circuit breaker modelling

Figure 2: Schematic diagram of ABB hybrid circuit breaker model

Figure 2 illustrates the schematic diagram of the hybrid circuit breaker of the type designed by ABB. LCS is the Load Commutation Switch; UFD is the Ultra-Fast Disconnector. The detailed operation algorithm of this hybrid circuit breaker can be found in [7]. Upon receiving the signal to open the breaker, the LCS will turn off to commutate load current from the LCS into the main breaker. Once the current is fully commutated to the main breaker, the UFD will open. When the UFD reaches the fully open position, the main breaker will then interrupt the load current. After the main breaker turns off, the DC side fault is isolated. At this stage, the disconnecting circuit breaker will open.

2.2 Onshore two-level converter modelling

The onshore two-level converters are modelled with switched IGBTs, phase reactors, transformers, HVDC cable and DC link capacitors. For the converter control, several control methods have been proposed for the multi-terminal DC system, like voltage margin control, voltage droop control, etc. [17] [18]. In terms of the onshore converter control, this paper employs the voltage droop control and the control structure is from [19].

2.3 Offshore two-level converter modelling

In terms of the offshore converter control, [20] suggests that the windfarm side VSC converter is normally configured to support the offshore AC grid voltage and frequency. This control configuration is used in this paper. Additionally, based on [21], a second order filter has been implemented and placed at the AC side of the offshore converter. This filter is employed to filter out the converter switching harmonics.

2.4 DC chopper modelling

DC choppers have been connected at each onshore converter’s DC terminals to regulate the DC link voltage; the DC chopper is implemented based on [22].

2.5 MMC modelling

In terms of the MMC modelling, different types of MMC model have been proposed in [23-25]. Notably are the Accelerated Model (AM), Detailed Equivalent Model (DEM) and Traditional Detailed Model (TDM). In this paper, the DEM is used for the MMC modelling, as the DEM can provide identical system response when compared to the TDM, but takes less time to run and the DEM can give more accurate simulation results than the AM [25]. In this paper, in the implemented MMC model, there are 30 sub-modules in each phase arm which could provide 31 voltage levels. The MMC station is set to control the active and reactive power flow. Figure 3 shows the control structure for such an MMC system, modified from [26].

![Converter switching control diagram](image)

**Figure 3:** Control structure of the MMC system (modified from [26])
3 Fault Current Limiter Modelling

This paper will discuss the modelling of the resistive type superconducting FCL (SCFCL).

For certain materials, such as YBCO and BSCCO, when the temperature in these materials drops below a critical temperature and the current density of these materials is lower than the critical current density and the magnetic field across the materials is lower than the critical magnetic field, these materials will exhibit zero resistivity to DC [27]. With a properly designed cooling system for these materials, they can be used as elements in power systems which can limit fault current. During normal operation, the load current is lower than the critical current and the temperature of these materials stays below the critical temperature, which means the normal operation losses of the conductor are approximately zero. When a short-circuit fault occurs, the fault current starts to increase. Once the fault current is greater than the critical current, the SCFCL starts to develop some resistance. Due to the presence of this resistance, the conductor is heated rapidly and the temperature of the conductor will rise above the critical temperature. After that, the resistance of the conductor increases further and the fault current will be further limited.

There are three methods to model the SCFCL.

3.1 Two-state Resistor Modelling Method (Method 1)

This type of SCFCL modelling is proposed in [28]. During normal operation, the resistance of the SCFCL, $R_{SC}$, is approximately zero. After the fault occurs, $R_{SC}$ is instantly increased to a fixed non-zero value to limit the fault current. The SCFCL is effectively modelled by a two-state resistor. For this SCFCL modelling method, the system transient current response is inaccurate, since the model does not consider the dynamic response of the SCFCL during the fault transient period.

3.2 “Exponential” SCFCL Modelling Method (Method 2)

To overcome the drawback of the two-state resistor modelling, the “exponential” SCFCL model is proposed in [29, 30]. Based on this method, when the fault occurs, $R_{SC}$ develops over time and the relationship between time, $t$, and resistance, $R_{SC}$, is:

$$R_{SC} = R_{max}(1 - e^{-t/T_{SC}})$$

$R_{max}$ is the maximum quench resistance. $T_{SC}$ is the time constant for the resistance of the superconductor to increase from approximately 0 Ω to the maximum quench resistance.

As can be seen in the first graph of Figure 5, with the help of the SCFCL, the fault current is limited to a low value. There is a delay in the changing of the SCFCL resistance; this is because it takes a finite amount of time for the fault current to increase beyond the critical current. Once the fault current is beyond the critical current, the resistance of the SCFCL will start to increase to quench the fault current.

However, there are some drawbacks when using this modelling method for SCFCL. Based on this method, after the SCFCL quenches the fault current, the resistance of the SCFCL will increase to a fixed value; this is not accurate as physically the resistance of the SCFCL will vary during the quenching stage. In addition, this modelling method cannot provide any information about the temperature of the SCFCL. Knowing the temperature of the SCFCL during the quenching stage is important as this can help the design of the SCFCL cooling system.

3.3 Detailed SCFCL Modelling Method (Method 3)

Based on [14, 16, 31], the conduction state of the superconductor can be divided into three regions: flux-creep, flux-flow and normal conducting regions. In the flux-creep region, the resistance of the SCFCL is zero. If the current density of the SCFCL exceeds $J_0$, which is the critical current density of the SCFCL in this case, the SCFCL will transit to the flux-flow region and the resistance of the SCFCL will rapidly increase. In the flux-flow region, the SCFCL will quickly be heated up by the current. When the temperature of the SCFCL increases above the critical temperature of the SCFCL, the SCFCL will enter the normal conducting stage.
and the resistance of the SCFCL will be determined by the temperature.

During each conduction stage, the relationship between $E$ and $J$ are illustrated below. $E$ is the electric field across the SCFCL and $J$ is the current density of the SCFCL.

1) Flux-creep stage (Superconducting stage):

$$E = E_C(1/J_c(T))^\alpha$$

2) Flux-flow stage (Quenching stage):

$$E = E_0(E_C/E_0)^{\beta/(77 K)}I_c(T_c)/(J_0(J_0/T_c - T(t))/J_0(T_c - 77 K))$$

$$J_c(T) = J_{c-77K}(T_c - 77 K)/(T(t) - T_c)$$

$$R_S = E/\eta SC/(J(t)\eta SC)$$

(1)

3) Normal conducting stage:

$$E(t, T) \approx \rho T_c (T/T_c)$$

$$R_S = E/\eta SC/(J(t)\eta SC)$$

$$\rho T_c (T/T_c)$$

In the simulation, it can be assumed that the resistance is zero during the superconducting stage. $\alpha$ is the flux-creep region constant and $\beta$ is the flux-flow region constant.

Please see the appendix for the values chosen to be used in the SCFCL modelling. Figure 6 illustrates the algorithm of the implemented superconductor model in PSCAD. Method 3 is used from this point on and example results are shown in Figure 9 and Figure 11.

4 DC protection system design

Various methods have been proposed for DC fault detection, such as overcurrent, derivative, and wavelet transforms, etc. [33, 34]. The continuous wavelet transform (CWT) method has been chosen to conduct DC protection in this paper. The continuous wavelet transform takes the form:

$$CWT = \int_{-\infty}^{+\infty} f(t) \cdot \Psi_{ab}(t)dt,$$

where $\Psi_{ab}(t) = (1/\sqrt{a})\Psi(t-b/a)$.

where $a$ is the scale or window size, and $b$ is the position. A DC fault can be detected if the obtained wavelet coefficient is greater than the threshold value.

Figure 7 shows the DC protection algorithm where 2 of 3 signals are used to detect a fault. This algorithm is modified from [33]. Figure 8 shows the location of each measured signal in Figure 7. The threshold value set to perform the DC protection is determined through sensitivity study for the system shown in Figure 1. When obtaining voltage and current signals, based on [35, 36], the voltage measurement delay ($T_1$) is around 33 microseconds and the current measurement delay ($T_2$) is around 40 microseconds.

5 Performance of SCFCL in DC protection

With respect to Figure 1, a pole-to-pole fault is introduced 20 km away from converter station A at 3 seconds. The DC system protection is achieved by using the DC circuit breaker, the SCFCL and the protection system shown in Figure 7.

Figure 9 shows the system fault current response comparison between the case when the SCFCLs are employed in the system and the case when there are no SCFCLs in the system. It can be seen from the first graph in Figure 9 that, with the help of the SCFCL, the peak fault current the DC circuit breaker needs to interrupt is reduced from ~8 kA to ~7 kA, which means a breaker with lower current rating can be used in this case. The fourth graph in Figure 9 is the resistance response of the SCFCL. It can be seen from this graph that, as
the fault current increases above the critical current (1.8 kA in this case), the resistance of the SCFCL \( R_{SCFCL} \) rapidly develops. The initial oscillation in \( R_{SCFCL} \) is because as \( R_{SCFCL} \) increases, at one point \( R_{SCFCL} \) will be approximately the same value as \( R_{shunt} \), after which more fault current will be diverted to flow through \( R_{shunt} \) as \( R_{SCFCL} \) and \( R_{shunt} \) are connected in parallel, as shown in Figure 10.

Figure 9: Simulation results of the fault current response with and without SCFCL

Less current will flow through \( R_{SCFCL} \) and based on SCFCL flux-flow state equations (see Equation (1)) \( R_{SCFCL} \) will reduce slightly. As \( R_{SCFCL} \) becomes smaller than \( R_{shunt} \), more current will flow through \( R_{SCFCL} \) to heat the SCFCL. In the meantime, the total fault current is still increasing, therefore, \( R_{SCFCL} \) will increase again. When the temperature of the SCFCL (\( T_{SCFCL} \)) increases above the critical temperature, which is 95 K in this case, the resistance of the SCFCL will be determined by \( T_{SCFCL} \), see Equation (2). As the superconductor is heated up by the fault current, \( T_{SCFCL} \) is increasing, and then \( R_{SCFCL} \) will increase. Later, when \( R_{SCFCL} \) is significantly higher than \( R_{parallel} \), most of the fault current will flow through the shunting resistor. Therefore, \( T_{SCFCL} \) becomes steady and \( R_{SCFCL} \) will not increase further.

Figure 10: Schematic diagram showing the configuration of the SCFCL

It can also be seen that, in terms of the fault current limiting, initially, there is no significant difference between the case when the SCFCL is used and the case when the SCFCL is not used. This is due to the resistance of the SCFCL not being high enough to limit the fault current, the fault current is only limited when the SCFCL reaches the normal conducting stage, as the resistance of the SCFCL in this stage is significant. There is a trade-off between the value of \( R_{shunt} \) and the amount of fault current the SCFCL can limit. The higher the value of \( R_{shunt} \), the lower the fault current can be limited to, but the voltage and temperature across the SCFCL will be higher.

Figure 11: Simulation results of the fault current response without shunt resistor

If a shunt resistance is not placed in parallel with the SCFCL to limit fault current, Figure 11 shows the SCFCL response to the fault current under this circumstance. Compared with Figure 9, the fault current is further reduced to around 4.5 kA in Figure 11. However, this is achieved at the expense of a much higher voltage across the SCFCL during the fault period, as 500 kV in Figure 11 compared with 110 kV in Figure 9. In addition, the temperature of the SCFCL rises to around 500 K in the case when there is no shunt resistor in parallel with the SCFCL, this may damage the SCFCL [37]. Therefore, the shunt resistor needs to be designed to give the compromise between SCFCL fault current quenching capability and voltage and temperature requirements of the SCFCL.

6 Conclusions

This paper conducts a study on the impact of the FCL exerts on the DC system protection. A detailed SCFCL model is used, together with the hybrid circuit breaker and the CWT algorithm to achieve the DC protection. The main findings are:

- The “Exponential” SCFCL modelling method cannot provide information about the temperature of the SCFCL during quenching stage.
- The resistive type SCFCL can be used in series with DC circuit breaker to help with DC fault protection. With the help of the SCFCL, the current rating of the DC circuit breaker can be reduced.
- A parallel resistance is needed for SCFCL to prevent it from experiencing overvoltage and overheat.

Acknowledgements

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References


Table 1: List of parameters used in SCFCL

<table>
<thead>
<tr>
<th>Variable</th>
<th>Definition</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>( E_c )</td>
<td>constant value</td>
<td>1( \mu )V/cm</td>
</tr>
<tr>
<td>( E_0 )</td>
<td>Critical electric field across the SCFCL</td>
<td>0.1V/m</td>
</tr>
<tr>
<td>( I_{SC} )</td>
<td>Current density where ( E = 1\mu V/cm )</td>
<td>1.5 \times 10^{-3} A/m²</td>
</tr>
<tr>
<td>( T_c )</td>
<td>Critical temperature of the SCFCL</td>
<td>95 K</td>
</tr>
<tr>
<td>( T_a )</td>
<td>Ambient temperature of the SCFCL</td>
<td>77 K</td>
</tr>
<tr>
<td>( a )</td>
<td>The flux-creep region constant</td>
<td>6</td>
</tr>
<tr>
<td>( \beta )</td>
<td>The flux-creep region constant</td>
<td>4</td>
</tr>
<tr>
<td>( \rho_{SC} )</td>
<td>Conducting resistivity of the SCFCL at ( T_c )</td>
<td>1 \times 10^{-5} \Omega m</td>
</tr>
<tr>
<td>( k )</td>
<td>The coefficient of heat transfer to cooling reservoir</td>
<td>1.5 \times 10^{-4} W/K²m²</td>
</tr>
<tr>
<td>( c_v )</td>
<td>the superconductor volumetric specific heat</td>
<td>1.0 \times 10^{6} J/Kg K²</td>
</tr>
<tr>
<td>( l_{SC} )</td>
<td>Length of the SCFCL</td>
<td>200 m</td>
</tr>
<tr>
<td>( R )</td>
<td>Radius of the SCFCL</td>
<td>3.48 \times 10^{-3} m</td>
</tr>
<tr>
<td>( R_{parallel} )</td>
<td>Shunt resistance in parallel with the superconductor</td>
<td>20 \Omega</td>
</tr>
</tbody>
</table>

Appendix