Abstract—Float-zone silicon is usually assumed to be bulk defect-lean and stable. However recent studies have revealed that detrimental defects can be thermally activated in float-zone silicon wafers and lead to a reduction of carrier lifetime by up to two orders of magnitude. A robust methodology which combines different characterization techniques and passivation schemes is used to provide new insight into the origin of degradation of 1 Ω·cm n-type phosphorus doped float-zone silicon (with nitrogen doping during growth) after annealing at 500 °C. Carrier lifetime and photoluminescence experiments are first performed with temporary room temperature surface passivation which minimizes lifetime changes which can occur during passivation processes involving thermal treatments. Temperature and injection dependent lifetime spectroscopy is then performed with a more stable passivation scheme, with the same samples finally being studied by deep level transient spectroscopy. Although five defect levels are found with deep level transient spectroscopy, detailed analysis of injection-dependent lifetime data reveals that the most detrimental defect levels could arise from just two independent single-level defects or from one two-level defect. The defect parameters for these two possible scenarios are extracted and discussed.

Index Terms—Float-zone, silicon, defects, recombination, lifetime, DLTS

I. INTRODUCTION

FLOAT-zone (FZ) silicon is considered to have high purity and little contamination [1]. For photovoltaic applications, FZ silicon wafers are often used for high efficiency solar cells [2]–[4]. FZ silicon is often assumed to be stable and bulk defect-free, thus it has been used as control materials in many studies of defects or degradation phenomena in silicon grown by other techniques [5]–[7]. FZ silicon wafers are also frequently used for the studies of silicon surface passivation quality [8]–[10], and for the parameterization of intrinsic recombination in silicon [11].

However, recent research has found that boron-doped p-type FZ silicon can suffer from light-induced degradation at elevated temperature (around 75 °C) [12], [13]. The degradation is found to be similar to the ones observed in multi-crystalline silicon [14], [15].

Apart from this light-induced degradation, it has been revealed that bulk defects with strong recombination activity can be formed in FZ silicon upon thermal processing. These defects are found to be activated by heat-treatments in the temperature range from 450 to 700 °C [16]. Unlike the light-induced degradation in FZ silicon which was mainly identified in p-type material (except that a recent study observed a weak degradation in 200 Ω·cm n-type FZ silicon [17]), these thermally activated defects can have a substantial impact in both n-type and p-type FZ silicon [16], [18]. After formation, these defects can be permanently annihilated by an annealing at temperature above 1000 °C for 30 mins. Further annealing at lower temperatures does not result in re-activation of the defects [18]. A recent study showed that the defect activation takes places on sub-second time scale and illumination with photon energy above the bandgap can decrease the onset temperature of degradation [19]. The defect deactivation is also found to occur within a short period of time (1 min at 1000 °C). However, a prolonged high temperature annealing is required for a complete and irreversible defect annihilation [19].

These thermally activated defects are very detrimental. A full activation can reduce the lifetime of minority carriers in silicon wafer from above a millisecond to about 10 μs [16], [18]. It should be noted that the deposition temperatures of several common dielectrics, such as silicon nitride (SiNₓ) via plasma-enhanced chemical vapor deposition (PECVD), can be in the temperature range where the recombination active defects are formed. Activation anneals for aluminum oxide surface passivation are also often performed in this temperature range.
Previous studies found that these thermally activated defects can be partially passivated via hydrogenation \cite{19}, \cite{21}, \cite{23}. Therefore, these defects could be activated during the surface passivation process, but also be partially passivated by the hydrogen introduced during the dielectric deposition process. It was also shown that the partial passivation provided by hydrogen is only temporary \cite{19}. The assumption that FZ silicon is bulk defect-free is therefore not always a correct one.

The origin of these thermally activated defects is not fully understood yet. Several studies have indicated that they are related to vacancies formed in the wafers during the FZ silicon growth \cite{16}, \cite{18}, \cite{21}, \cite{23}. From photoluminescence (PL) images of the degraded FZ silicon wafers, it has been found that the activated defects have a much higher concentration in the center circular area than in the periphery of the wafers \cite{16}. This correlates with the distribution of vacancy-related defects in FZ silicon grown with a relatively high growth speed \cite{24}–\cite{27}. Moreover, a few studies have also indicated that nitrogen doping during the FZ silicon growth plays a significant role in the formation of recombination active defects \cite{18}, \cite{21}, \cite{23}. Via secondary ion mass spectrometry, a recent study showed that the permanent defect annihilation requires effusion of nitrogen impurities from the wafer, indicating the involvement of nitrogen in the defect formation \cite{19}.

The detailed electrical properties of the defects have been mainly investigated by deep level transient spectroscopy (DLTS) and minority carrier transient spectroscopy (MCTS) \cite{16}, \cite{18}, \cite{19}, \cite{23}. Several defect levels have been assigned to the thermally activated defects. However, the relative recombination activities of these levels have not been fully determined. Therefore, it is not clear which (if any) of these defect levels are responsible for the severe reduction of carrier lifetime.

Lifetime spectroscopy is a defect characterization technique sensitive to recombination active defects \cite{28}. Thus, it is suitable for characterizing the most detrimental defect levels. Injection dependent lifetime spectroscopy (IDLS) has previously been applied to study thermally activated defects in FZ silicon \cite{21}. However, due to the fundamental ambiguity of IDLS, the defect parameters were not extracted and only a constraint of the defect parameters were given. By measuring injection dependent lifetime at various temperatures (TIDLS) \cite{29} or at various doping concentrations ($N_{\text{dop}}$-IDLS) \cite{28}, \cite{30}, it is possible to reduce the ambiguity of defect parameterization in IDLS \cite{28}, \cite{31}.

In this study, we combine TIDLS with DLTS/MCTS to characterize the most detrimental defect levels among the thermally activated defects in $n$-type FZ silicon. We also provide a thorough analysis of their electrical properties.

### II. EXPERIMENTAL METHODS

In this study, a set of phosphorus doped $n$-type FZ silicon wafers grown in a nitrogen ambient from the same manufacturer was used. The wafers were circular with a diameter of 100 mm, 190 µm thick and have a resistivity of 1 Ω-cm. The wafers were divided into three groups (labeled as Groups A, B and C). Each group contains three to four wafers to improve the statistics of the measurements. Wafers in Group A were not annealed. Wafers in Group B were annealed at 500 °C in a nitrogen ambient for 30 mins. According to previous studies \cite{16}, \cite{18}, \cite{19}, this annealing should activate the defects and lead to a significant lifetime reduction in the wafers. Wafers in Group C were annealed at 1000 °C in a nitrogen ambient for 30 mins. The annealing was completed in a tube furnace with no above-bandgap light incident on the samples other than that from the furnace element (which is negligible at 500 °C). According to previous studies, this annealing should stabilize the lifetime in the FZ silicon and prevent any further degradation due to the thermally activated defects \cite{16}, \cite{18}, \cite{19}.

For the first characterization stage, after the corresponding annealing process, all the wafers went through a superacid-derived passivation process based on bis(trifluoromethanesulfonimide) (TFSI) in 1, 2-dichloroethane (DCE) \cite{32}, \cite{33}. This passivation scheme has been shown to provide an excellent passivation quality with a surface recombination velocity (SRV) around 1 cm/s for wafers with similar resistivity \cite{32}. It is also stable for enough time to undertake room-temperature PL imaging and injection dependent lifetime measurements. Moreover, it is a process conducted at room temperature, thus, no thermal budget or bulk hydrogen is added to the wafers, which is very important for the investigation of defects that are sensitive to processing at elevated temperatures. PL imaging was performed with a BT Imaging LIS-L1 system and lifetime measurements were made with a Sinton WCT-120 lifetime tester.

The superacid-derived passivation is not stable enough for TIDLS measurements, so, at the second characterization stage, all wafers were re-passivated by amorphous silicon (a-Si) via PECVD. The wafers were RCA (Radio Corporation of America) cleaned before the deposition. The deposition temperatures were kept below 200 °C to minimize the thermal budget on the samples and the potential hydrogen induced passivation of the defects. The passivation quality and stability of the a-Si passivation scheme were tested with a separate set of FZ wafers. It was found that the a-Si passivation can achieve an SRV below 10 cm/s and remain stable up to 200 °C. After the a-Si passivation, TIDLS measurements of the wafers were performed at temperatures in the range from −50 °C to 150 °C with a customized temperature dependent carrier lifetime tester \cite{34}. Due to the non-uniform distribution of the defects, the lifetime of the degraded wafers (Group B) were measured in the center area (using a circular sensor area with diameter of 28 mm) where the defects have a relatively high concentration and uniform distribution. After the lifetime measurements at elevated temperatures, the carrier lifetime of the samples was re-measured at room temperature to check if the sample lifetime had changed during the TIDLS measurements.

For the third characterization stage which utilized DLTS and MCTS measurements, the a-Si layers were stripped off from the samples with a mixture of 49% hydrofluoric acid (HF) and 69% nitric acid (ratio of 1:10) for two minutes, followed by rinsing in deionized water. Schottky barrier diodes (SBDs) and ohmic contacts were fabricated by thermal evaporation of metal layers.
onto the sample surfaces subjected to cleaning and dipping in diluted HF prior to the evaporation. For the SBDs, gold was evaporated through a shadow mask. For the ohmic contact, an aluminum layer was evaporated at the back surface. For MCTS measurements, an open area at the back side of the samples was left for the optical excitation using a 940 nm light emitting diode. All these processes were undertaken close to room temperature.

### III. RESULTS AND DISCUSSIONS

#### A. Thermal activation of the defects

Room temperature lifetime and PL imaging measurements were performed on all wafers with superacid-derived surface passivation. The calibrated PL images (under one sun illumination) of one typical wafer from each group are shown in Fig. 1. The measured injection dependent lifetime curves are shown in Fig. 2.

As can be seen, the wafer from Group B (annealed at 500 °C) shows a much lower lifetime in its center compared to its periphery. The wafers from Group A (not annealed) and Group C (annealed at 1000 °C), on the contrary, both show uniformly high carrier lifetime. Regarding the injection dependent lifetime, wafers from Group A and C show similar lifetime curves with lifetime above 1 ms across the measured injection range. The lifetime measured at the center of the wafers from Group B is around 10 µs, two orders of magnitude lower than that of wafers from Groups A and C.

These results agree with the findings in previous studies [16], [18], [21], [23]. Annealing at 500 °C activates bulk recombination-active defects in FZ silicon, whereas annealing at 1000 °C does not. Furthermore, the activated defects have a much higher impact in the center area of the wafer than at the periphery.

#### B. DLTS and MCTS: energy levels of the activated defects

![Fig. 1. Calibrated PL images for one 100 mm diameter wafer from each group with room temperature superacid-derived surface passivation. The wafer from Group A was not annealed, the wafer from Group B was annealed at 500 °C for 30 min, and the wafer from Group C was annealed at 1000 °C for 30 min.](image)

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Fig. 3. (a) DLTS spectra measured on a representative sample from each group. ΔC is the change of capacitance in the DLTS measurements. For sample from Group B, DLTS spectra measured from two different locations on the wafer are shown. The spectra are intentionally shifted in the vertical direction for clarity. The spectra were recorded with a reverse bias voltage $U_b$ of −9 V, a filling pulse voltage $U_f$ of −4 V, filling pulse length $t_f$ of 1 ms and an emission rate of 50 s⁻¹. (b) An Arrhenius plot of the five identified electron emission signals from the center of a sample in Group B. $E_n$ is the electron emission rate, $k_B$ is the Boltzmann constant and $T$ is absolute temperature.

The measured DLTS spectra of one typical sample from each group are shown in Fig. 3(a). As can be seen, four clear peaks appear in the DLTS spectrum recorded from the center of the degraded wafer (Group B). The relatively strong peak with its maximum at around 180 K also occurs in the spectrum measured near the edge of this degraded wafer, however, with a much smaller amplitude. A scale bar for the estimation of defect density $N_t$ is also given in the figure. None of the peaks are observable in the DLTS spectra measured on wafers of
Groups A and C. For clarity, the DLTS spectra measured on the samples from the edge area of the wafers from Groups A and C are not shown in Fig. 3(a), but they can be found in Fig. S1 of the support information. A Laplace-DLTS analysis [35], [36] indicates that the broad peak at around 180 K results from two electron emission signals, i.e., it is a convolution of two peaks. Therefore, in total there are five electron emission signals detected in samples from the center of the degraded wafers, labelled as E1 to E5. Combining with the PL imaging results, it is likely that these five emission signals are related the thermally activated defects in FZ silicon.

Electron emission rates for the five detected traps were measured as a function of temperature. The activation energies $E_{na}$ and apparent electron capture cross sections $\sigma_{na}$ have been extracted from the Arrhenius plots shown in Fig. 3(b) and are listed in Table I. The detected defect levels are similar to the ones identified by Grant et al. [16], yet they differ from the ones recognized by Mullins et al.[23]. The detected E1, E2 and E3 are similar to the ones detected by Hiller et al. [19], yet the detected E4 and E5 are different. One possible reason for these differences is that the activated defects in FZ silicon from different manufacturers might be different, due to the difference in the silicon growth condition. The FZ wafers used in this study are from the same manufacturer as the ones used by Grant et al. [16] and have nitrogen present in the growth ambient to suppress the formation of extended defects. In the DLTS spectra measured by Grant et al. [16], the E3 emission signal has the highest peak height, whereas in our measurement, the E4 and E5 emission signals have the highest amplitude.

**TABLE I**

<table>
<thead>
<tr>
<th>Electron Trap</th>
<th>$E_{na}$ [eV]</th>
<th>$\sigma_{na}$ [cm$^2$/s]</th>
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<tbody>
<tr>
<td>E1</td>
<td>0.16±0.005</td>
<td>1.5×10$^{-15}$</td>
</tr>
<tr>
<td>E2</td>
<td>0.20±0.005</td>
<td>3.7×10$^{-16}$</td>
</tr>
<tr>
<td>E3</td>
<td>0.28±0.005</td>
<td>4.2×10$^{-15}$</td>
</tr>
<tr>
<td>E4</td>
<td>0.41±0.01</td>
<td>1.5×10$^{-14}$</td>
</tr>
<tr>
<td>E5</td>
<td>0.43±0.01</td>
<td>1.2×10$^{-14}$</td>
</tr>
</tbody>
</table>

The electron capture characteristic of the five detected defect levels were also measured by varying the filling pulse length $t_p$. The actual $\sigma_n$ of the E5 trap has been found to be temperature dependent according to $\sigma_n = \sigma_{na} \exp \left(-\frac{E_{na}}{k_B T}\right)$ with a pre-exponential factor $\sigma_{na} = 6.9 \times 10^{-16}$ cm$^2$ and an energy barrier $E_{ao} = 0.19$ eV. Unfortunately, since the changes in peak heights for E1 to E4 traps have been too small within the available range of $t_p$ in our DLTS setup (1 µs to 1 ms), we have not been able to extract their $\sigma_n$ accurately. The DLTS spectra recorded with different $t_p$ can be found in Fig. S2 of the support information. The details regarding the extraction of the actual $\sigma_n$ for the E5 trap can also be found in Fig. S3 of the support information.

Since DLTS on Schottky diodes only detects defect levels located in the majority carrier bandgap half (the upper half for n-type silicon), MCTS measurements were also performed in order to identify defect levels in the minority carrier bandgap half [37]. The measured MCTS spectra for one typical sample from each group are shown in Fig. 4(a). As can be seen, a peak related to hole emission is detected in the MCTS spectrum of the degraded sample (Group B), whereas no clear hole emission signal has been detected in the spectra of samples from Groups A and C. Therefore, this hole emission signal (labelled as H1) can also be correlated with the thermally activated defects.

The activation energy $E_{pa}$ and apparent hole capture cross section $\sigma_{pa}$ of the H1 trap were also extracted by measuring the hole emission rates at various temperature. The associated Arrhenius plot is shown in Fig. 4(b). $E_{pa}$ is found to be 0.35±0.02 eV, while $\sigma_{pa}$ is found to be 4.0×10$^{-15}$ cm$^2$. Unfortunately, due to the relatively low defect concentration, the actual hole capture cross section $\sigma_p$ could not be accurately determined from direct capture measurements.

![Fig. 4.](image)

(a) MCTS spectra for samples from the center of a representative wafer from each group. $N_d$ is the doping concentration, $C_b$ is the reverse bias capacitance, $E_p$ is the hole emission rate. (b) The Arrhenius plot of the identified hole emission signal.

Based on the above DLTS and MCTS results alone, it is not possible to determine which level among the detected E1 to E5 electron traps and the H1 hole trap is the most detrimental one, or which level/levels should be responsible for the dramatic lifetime reduction of the degraded wafers.

C. **TIDLS: characterization of the dominant defect level**

As lifetime spectroscopy allows defect parameters to be extracted directly from the measured lifetime, it is very sensitive to the dominant lifetime-degrading defect level(s) in the sample. Therefore, in this study, we use TIDLS to identify the dominant recombination-active defect levels of the thermally activated defects in FZ silicon.

It is first important to demonstrate that the temperature dependent measurements themselves do not change the
properties of the sample under investigation. The room temperature lifetime curves of the degraded wafer before and after TIDLS measurements are shown in Fig. 2. As can be seen, the temperature dependent measurements do not change the lifetime significantly, indicating the heating during the TIDLS measurements does not change the concentration or properties of the dominant defects. Furthermore, the lifetime curve of the degraded wafer with a-Si passivation is almost identical to the one with superacid-derived passivation, indicating that (1) the effective lifetime of the sample is dominated by the activated bulk defects with these two different surface passivation techniques; and (2) the deposition of a-Si has not resulted in a change of the properties of the dominant defects.

For TIDLS analysis, we need to extract the defect associated recombination lifetime from the measured effective lifetime. As all wafers have the same intrinsic lifetime (due to Auger and band-to-band radiative recombination) by virtue of having the same doping, and, assuming that the surface recombination is constant, the following equation can be used to calculate the defects associated recombination lifetime:

\[ \frac{1}{\tau_{\text{defects}}} = \frac{1}{\tau_B} - \frac{1}{\tau_C} \tag{1} \]

where \( \tau_B \) and \( \tau_C \) are the effective lifetime measured from the center of wafers from Group B (annealed at 500 °C) and wafers from Group C (annealed at 1000 °C), respectively. At all the measurement temperatures, \( \tau_B \) is always nearly two orders of magnitude lower than \( \tau_C \), thus \( \tau_{\text{defects}} \) is very close to the measured \( \tau_B \). This indicates that \( \tau_B \) is dominated by the recombination lifetime of the thermally activated defects. This also relaxes the previous assumption regarding identical intrinsic lifetime and SRV as they have little impact on the accuracy of the extracted \( \tau_{\text{defects}} \). Comparisons of \( \tau_B, \tau_C \) and \( \tau_{\text{defects}} \) at all measured temperatures can be found in Fig. S4 of the support information. The extracted defect lifetime curve at all the measured temperatures are shown in Fig. 5. As can be seen, \( \tau_{\text{defects}} \) increases with the measurement temperature.

Fig. 5. The extracted lifetime calculated from (1) due to defects formed by 30 min annealing at 500 °C for all the lifetime measurement temperatures (−50 °C, −25 °C, 0 °C, 30 °C, 50 °C, 100 °C, 125 °C and 150 °C).

To gain insight into the recombination physics from lifetime measurements, we have re-plotted \( \tau_{\text{defects}} \) against a new variable \( Y \equiv p/n \), where \( n \) and \( p \) are the concentration of free electrons and free holes respectively. According to Murphy et al. [30], recombination lifetime resulting from a single-level defect which obeys Shockley-Read-Hall (SRH) recombination statistics [38], [39] is a linear function of \( Y \) in n-type material. If the \( \tau_{\text{defects}} - Y \) plot is not linear, it indicates that \( \tau_{\text{defects}} \) is impacted by more than one defect level. The \( \tau_{\text{defects}} - Y \) plot at 30 °C is shown in Fig. 6, while the plots at other temperatures can be found in Fig. S5. It is found that the \( \tau_{\text{defects}} - Y \) plots at all the measurement temperatures are concave curves and not straight lines.

Fig. 6. \( \tau_{\text{defects}} - Y \) plot at 30 °C. The fitting of the data with two single-level defects or one two-level defect are overlaid.

This concave shape of \( \tau_{\text{defects}} - Y \) plots can be explained by the existence of two or more single-level defects or a multiple-level defect, or a mixture of them. We have tried to fit the experimentally derived data with two possible scenarios: (1) \( \tau_{\text{defects}} \) is dominated by two single-level defects; (2) \( \tau_{\text{defects}} \) is dominated by a two-level defect following the Sah-Shockley recombination statistics [40]. The fitting curves of these two scenarios are also shown in Fig. 6 (as well as in Fig. S5 for the other temperatures). As can be seen, both fitting approaches provide a good fit quality. In the following extraction of the dominant defect parameters, both possibilities are considered.

D. Defect parameterization

In this section, we combine the information from DLTS/MCTS and TIDLS to extract the electrical properties (\( \sigma_n \) and \( \sigma_p \) and energy level \( E_i \)) of the dominant recombination active defect levels among the thermally activated defects in FZ silicon. From DLTS/MCTS measurements, a judgment of the relative recombination activity of the detected defect levels is not certain. Moreover, because of significant changes in entropy resulting from charge state changes for some defects, the extracted apparent capture cross section can differ significantly from the actual capture cross section [41], [42]. The activation energy for carrier emission can also be different from the actual defect energy level [41], [42]. From DLTS at a single temperature, the defect parameterization is ambiguous, i.e., it has an infinite number of solutions [31]. TIDLS can reduce this ambiguity, yet, it also suffers from the difficulty of temperature dependency of capture cross sections [28]. By combining the information from DLTS/MCTS and TIDLS, we expect to achieve a more accurate assessment of the electrical properties of the dominant defect level.
1) Two single-level defects

We first consider the possibility that \( \tau_{\text{defect}} \) is dominated by two single-level defects. For TIDLS analysis, we use a modified defect parameter solution surface (DPSS) method [43] to extract the solution space of defect parameters at each temperature [28]. For each temperature, we obtained all the combinations of \( E_t, N_t \sigma_n \) and \( N_t \sigma_p \) that can provide the best fit of the measured lifetime. These combinations can be illustrated by the so called DPSS curves (shown in Fig. S6). From Fig. 6, it appears that one of the single-level defects dominates most of the injection levels, whereas, the other defect only impacts the lifetime at very low injection conditions. The uncertainty in the extracted parameters for the less dominant defect can be significant, as a small uncertainty in the measured lifetime can propagate to a much larger uncertainty during the fitting. Therefore, in this study we only focus on the defect parameters of the more dominant defect.

<table>
<thead>
<tr>
<th>Label</th>
<th>Equation</th>
<th>Capture mechanism</th>
</tr>
</thead>
<tbody>
<tr>
<td>Constant</td>
<td>( \sigma(T) = \text{constant} )</td>
<td>Classical capture [44], or radiative capture [45]</td>
</tr>
<tr>
<td>Exponential</td>
<td>( \sigma(T) = \sigma_0 \exp\left(-\frac{E_n}{k_B T}\right) )</td>
<td>Multi-phonon emission capture [46]</td>
</tr>
<tr>
<td>Power law</td>
<td>( \sigma(T) = \sigma_0 T^{-\alpha} )</td>
<td>Cascade capture [47], or exciton-enhanced Auger capture [48, 49]</td>
</tr>
<tr>
<td>Mix</td>
<td>( \sigma(T) = T^{-2} \exp\left(\frac{\Delta E}{k_B T}\right) )</td>
<td>Two-stage cascade capture [50]</td>
</tr>
</tbody>
</table>

As mentioned above, from TIDLS we extracted the full solution space of \( E_t, N_t \sigma_n \) and \( N_t \sigma_p \) at each temperature. Therefore, for each assumed value of \( E_t \), the value of \( N_t \sigma_n \) and \( N_t \sigma_p \) at each temperature can be obtained. Since \( N_t \) is temperature independent, the temperature dependencies of \( \sigma_n \) and \( \sigma_p \) can be extracted. Four possible temperature dependencies of capture cross sections which have been identified in the literature, are summarized in Table II. Therefore, for each assumed \( E_t \), we can fit the extracted \( N_t \sigma_n(T) \) and \( N_t \sigma_p(T) \) with the four possible models. If at a certain value of \( E_t \), both \( N_t \sigma_n(T) \) and \( N_t \sigma_p(T) \) can be well fitted with one of these four models, this \( E_t \) can be assumed as a possible solution.

![Fig. 7](image)

Fig. 7. The lowest residual values of fitting \( N_t \sigma_n(T) \) and \( N_t \sigma_p(T) \) of the dominant defect using the four possible temperature dependencies. The different symbols indicate the different temperature dependency that provides the optimal fitting. The vertical dashed lines indicate the activation energies of the emission signals detected by DLTS and MCTS.

From Fig. 7, we can first exclude the possibility of \( H_1 \) as the dominant defect level. In the lower half of the bandgap, there is only a small range of \( E_t \) with solution. However, the activation energy extracted from MCTS for \( H_1 \) is far away from this range (an energy difference of around 0.1 eV). This deviation cannot be explained by the temperature dependency of \( \sigma_p(T) \). Within the solution range of \( E_t \) in the lower half of the bandgap, the optimal fitting for \( \sigma_p(T) \) is always the power law dependency and a power law temperature dependency of \( \sigma_p(T) \) will have only a small impact on the deviation of \( E_{pa} \) from the actual \( E_t \) (around 0.026 eV, see Fig. S7 in support information for more detailed analysis). This small impact cannot explain the large deviation of \( H_1 \) activation energy to the possible solution range identified by TIDLS. With a similar reason, we can also exclude to possibility of the \( E_4 \) being the dominant defect level.

The temperature dependency of \( \sigma_n(T) \) for \( E_5 \) has been previously determined by DLTS and showed an exponential law temperature dependence. Taking this temperature dependency into consideration, the corrected \( E_t \) for \( E_5 \) is obtained to be 0.24 eV below the conduction band edge \( E_c \). However, as can be seen from Fig. 7, at this corrected \( E_t \), the TIDLS results indicate that \( \sigma_n(T) \) should follow a power law. The fact that there is no solution around the value of \( E_5 \) and the fact that the temperature dependence of the TIDLS fit quality at a certain assumed \( E_t \). It can be noted that the symbols only exist in a certain range of \( E_t \) within the bandgap; this is because only in this range, the solution of \( E_t, N_t \sigma_n \) and \( N_t \sigma_p \) exists for all the measured temperatures (see Fig. S6). \( E_t \) values outside of this range are thus, excluded from the TIDLS solution space. Another point to note is that the plots in Figure 7 have only finite discrete data points. That is because the fitting here was done only to a finite number of \( E_t \). Actually, within the solution range, there are infinite possible values for \( E_t \). In Fig. 7 we also overlay the apparent defect levels identified by DLTS and MCTS as dashed lines.
contradicts the DLTS result excludes E3 as the dominant trap. Therefore, only either E1, E2 or E3 are likely to be the dominant defect level. However, the fitting quality of $N_e\sigma_n(T)$ for E1 is much lower than that for the E2 and E3, which makes it less likely to be the correct defect level. For E2 and E3, the overall fitting quality of $N_e\sigma_n(T)$ and $N_e\sigma_p(T)$ is similar and we will now investigate these two levels in more details.

The $N_e\sigma_n(T)$ and $N_e\sigma_p(T)$ extracted from TIDLS at E2 is shown in Fig. 8. The optimal fitting of $\sigma_n(T)$ is achieved by a power law model. The impact of this power law temperature dependency on the deviation of $E_{\text{int}}$ from the actual $E_t$ is estimated to be around 0.01 eV. The optimal fitting of $\sigma_p(T)$ is achieved by a mix of exponential law and power law. However, fitting with an exponential law also provides a reasonably good fitting quality, thus, both temperature dependency models are possible. The fitting curves obtained from these two models are shown in Fig. 8 for comparison. The defect parameters extracted from fitting of the TIDLS curves upon the assumption of the E2 trap as the dominant recombination defect level are summarized in Table III.

We now investigate the possibility of the E3 trap as the dominant defect level. The extracted $N_e\sigma_n(T)$ and $N_e\sigma_p(T)$ from the TIDLS at E3 is shown in Fig. 9. At the activation energy of E3, the optimal fitting for $\sigma_n(T)$ is achieved by the exponential law model. However, it can be seen that $\sigma_n$ does not increase monotonically with temperature, thus, a temperature independent $\sigma_n$ seems to be more reasonable. The optimal fitting of $\sigma_p(T)$ is achieved with the power law model.

The defect parameters extracted from fitting of the TIDLS curves upon the assumption of the E3 trap as the dominant recombination defect level are also summarized in Table III.

![Fig. 8](image)

**Fig. 8.** $N_e\sigma_n(T)$ and $N_e\sigma_p(T)$ extracted at the activation energy of E2 for the dominant defect in TIDLS analysis. The blue line indicates the power law fit the $N_e\sigma_n(T)$. The orange solid line indicates the fit of $N_e\sigma_p(T)$ with a mix of power law and exponential law. The orange dashed line indicates the fit of $N_e\sigma_p(T)$ with the exponential law model.

![Fig. 9](image)

**Fig. 9.** $N_e\sigma_n(T)$ and $N_e\sigma_p(T)$ extracted at the activation energy of E1 for the dominant defect in TIDLS analysis. The blue line indicates the average value of $N_e\sigma_n$.

![Fig. 10](image)

**Fig. 10.** Map of the sum of fitting residuals at each temperature with the assumption of a two-level defect. The two axes indicate the two energy levels of a two-level defect.

2) One two-level defect

Now we consider the case where the dominant defect is a two-level defect. As can be seen from Fig. 6, fitting under this assumption provides similar fitting quality to the case of two single-level defects. Therefore, it should not be ruled out as a
possibility.

Here we use the method of two-levels defect parameterization proposed by Zhu et al. [51, 52]. Similar to the case of two single-level defects, the lifetime curve at each temperature is fitted independently. For each combination of two energy levels, \( E_{t1} \) and \( E_{t2} \), the minimal fitting residual is illustrated in a two-dimensional map. The solution space of the defect parameters can be identified from the regions of the low fitting residuals. The fitting residual map for each measurement temperature can be found in Fig. S8. In Fig. 10, we present the sum of all the fitting residual maps. In this map, the regions of low residuals indicate the combinations of \( E_{t1} \) and \( E_{t2} \) that can provide good fitting quality to lifetime data at all measured temperatures.

In the regions of low fitting residuals in Fig. 10, we also fit the extracted \( N_t \sigma_{n1}(T) \), \( N_t \sigma_{p1}(T) \), \( N_t \sigma_{n2}(T) \) and \( N_t \sigma_{p2}(T) \) with the four possible temperature dependencies of the capture cross section mentioned above. The subscript “1” and “2” here indicate the first level and the second level, respectively (not to confuse with the subscript number in \( E_1 \) to \( E_5 \) and \( H_1 \)). In this study, the first level refers to the transition energy between the most positively charged state and the middle charge state, whereas the second level refers to the transition energy between the middle state and the most negatively charged state.

The map of minimal fitting residual for fitting \( N_t \sigma_{n1}(T) \) with the four possible models is shown in Fig. 11. The corresponding maps for \( N_t \sigma_{p1}(T) \), \( N_t \sigma_{p1}(T) \), \( N_t \sigma_{n2}(T) \) and \( N_t \sigma_{p2}(T) \) can be found in the support information. In Fig. 11, the activation energies of \( E_1 \), \( E_2 \), \( E_3 \) and \( H_1 \) are indicated by horizontal and vertical dashed lines as they have similar estimated defect density from DLTS/MCTS measurements. The possible combination of \( (E_{t1}, E_{t2}) \) should lie at the intersection of their corresponding lines (marked as green circles) except the ones from two identical levels (marked as red crosses).

From Fig. 11 and the other three maps for \( N_t \sigma_{p1}(T) \), \( N_t \sigma_{n2}(T) \) and \( N_t \sigma_{p2}(T) \), it is concluded that only for the combinations of \((E_1, H_1)\) and \((E_2, H_1)\) the fitting residuals for all four \( \sigma(T) \) are small. The extracted energy levels and \( \sigma(T) \) at these two possible combinations are summarized in Table IV. It should be noted that in both cases the \( \sigma_{n1}(T) \) and \( \sigma_{p2}(T) \) follow the power law temperature dependency, thus, it can be expected that the deviations between \( E_{t1} \) and \( E_{t1} \) and between \( E_{t2} \) and \( E_{t2} \) are small.

**E. Discussion**

The above analysis of the dominant recombination defect levels considers the two cases: (1) two single-level defects, and (2) one two-level defect. Both cases provide satisfying fitting to the measured TIDLS data. However, we cannot completely exclude the possibility of more complicated cases, such as more than two single-level defects or a combination of single-level defects and multi-level defects. The two scenarios analyzed above are the simplest models which can provide good fitting of the measured lifetime data.

By combining the results of TIDLS with the results of DLTS/MCTS measurements on the same samples, we have extracted the possible electrical parameters of the dominant defect levels. However, the above analysis assumes that the dominant defect levels are among the ones detected by the DLTS/MCTS; this might not be true. For example, in the case of boron-oxygen related defect responsible for light induced degradation of lifetime in Czochralski grown boron doped silicon wafers, the recombination defect level has not been reliably detected by DLTS, even though the defect has strong recombination activity as detected by lifetime measurements [53].

Finally, the above analysis also demonstrated the complexity of a full parameterization for a defect. Even though different techniques are used, we only identified several possible solutions of the defect parameters and are not able to make a

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**Table IV**

<table>
<thead>
<tr>
<th>Possibility 1</th>
<th>Possibility 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>( E_{t1} )</td>
<td>0.16±0.01 eV</td>
</tr>
<tr>
<td>( \sigma_{n1}(T) ) with ( \alpha = 1.78 )</td>
<td>( \sigma_{n1}(T) ) with ( \alpha = 1.99 )</td>
</tr>
<tr>
<td>( \sigma_{p1}(T) )</td>
<td>( T^{-2} \exp \left( \frac{\Delta E}{k_BT} \right) ) with ( \Delta E = -0.11 ) eV</td>
</tr>
<tr>
<td>( \sigma_{n2}(T) ) with ( \sigma_{n2}(T) ) with ( \sigma_{p2}(T) )</td>
<td>0.35±0.02 eV</td>
</tr>
<tr>
<td>( \sigma_{p2}(T) )</td>
<td>( \sigma_{p2}(T) ) with ( \alpha = 5.41 )</td>
</tr>
<tr>
<td>( k_1 = \sigma_{n1}/\sigma_{p1} ) at ( 300 ) K</td>
<td>0.035</td>
</tr>
<tr>
<td>( k_2 = \sigma_{n2}/\sigma_{p2} ) at ( 300 ) K</td>
<td>0.366</td>
</tr>
<tr>
<td>( \sigma_{n1}/\sigma_{n2} ) at ( 300 ) K</td>
<td>0.589</td>
</tr>
</tbody>
</table>
decisive determination of the defect parameters. A sketch of the possible energy level positions of the identified dominant defect is shown in Fig. 12. One of the major difficulties originates from the temperature dependencies of the capture cross sections. Doping dependent IDLS is an ideal technique to overcome this problem. However, it is essential to ensure that samples with various doping concentrations have the same defects. Previous studies indicate that these thermally activated defects could be different depending on the FZ silicon growth conditions [16], [18], [23]. Therefore, special care needs to be taken in the sample selection for doping dependent IDLS study.

![Fig. 12](image-url)  
*Fig. 12. A sketch of the energy level positions for the four possibilities of the dominant defect identified by a combined analysis of TIDLS and DLTS/MCTS.*

### IV. CONCLUSION

In this study, the thermally activated defects in n-type FZ silicon were investigated with a combination of TIDLS and DLTS/MCTS techniques. A thorough combined analysis of the data obtained by all the used techniques has been made to extract the electrical properties of the dominant recombination defect. From analysis of lifetime data, it was found that the most detrimental activated defects can be two single-level defects, or a defect with two energy levels. Both scenarios can provide satisfying fitting to the measured TIDLS data. The possible positions of energy levels and the temperature dependencies of capture cross sections for both scenarios were extracted. Measuring samples with various doping concentrations would be beneficial for a more decisive determination of the actual defect structure among these possibilities.

### REFERENCES


